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File 348:EUROPEAN PATENTS 1978-2007/ 200806
         (c) 2008 European Patent Office
File 349:PCT FULLTEXT 1979-2008/UB=20080131UT=20080124
         (c) 2008 WIPO/Thomson

Set       Items   Description
S1        28830    (CALL OR CALLS OR PHONECALL? ?)(3N)(DISTRIBUT???? OR MANAG-
             ????? OR HANDL??? OR ROUT???) OR CALL()(CENTER? ? OR CENTRE? -
             ?) OR BRANCH(3N)(EXCHANGE? ? OR MANAG?????)
S2         8691    (SPEECH OR VOICE OR AUDIO OR TELEPHONE OR PHONE OR AUTOMAT-
             ?)()(RESPONSE?? OR REPLY?) OR (SPEECH OR VOICE)()RESPONSE?? OR
              ARU OR VRU OR IVR OR VRS
S3      696227    EVENT? ? OR OCCURRENCE? ?
S4     1048839    WORKFLOW? ? OR FLOW? ? OR ROUTE? ? OR ROUTING OR HISTORY
S5     1243764    PRIORITY OR PRIORITIZ? OR PRIORITIS? OR IMPORTANT OR IMPOR-
             TANCE OR SIGNIFICANT OR SIGNIFICANCE
S6      218483    TASK? ? OR JOB? ? OR TRANSACTION? ?
S7     1453935    THREAD? ? OR PROCESS OR PROCESSES
S8      395294    QUEU???? OR BUFFER????
S9       22604    S3(10N)S4
S10      23532    S4(5N)S5
S11       9311    S3(10N)S6
S12      10569    S5(7N)S6
S13       2017    S6(10N)S8(10N)S7
S14          0    S9(50N)S10(50N)S11(50N)S12(50N)S13
S15          1    S1:S2/TI,AB,CM AND S9(50N)S10(50N)S11(50N)S12
S16          0    S1:S2/TI,AB,CM AND S9(50N)S10(50N)S11(50N)S13
S17          0    S1:S2/TI,AB,CM AND S9(50N)S10(50N)S12(50N)S13
S18          0    S1:S2/TI,AB,CM AND S9(50N)S11(50N)S12(50N)S13
S19          1    S1:S2/TI,AB,CM AND S10(50N)S11(50N)S12(50N)S13
S20         10    S9(50N)S10(50N)S11(50N)S12
S21          0    S9(50N)S10(50N)S11(50N)S13
S22          0    S9(50N)S10(50N)S12(50N)S13
S23          2    S9(50N)S11(50N)S12(50N)S13
S24          4    S10(50N)S11(50N)S12(50N)S13
S25          2    S1:S2/TI,AB,CM AND S9(50N)S10(50N)S11
S26          1    S1:S2/TI,AB,CM AND S10(50N)S11(50N)S12
S27          6    S1:S2/TI,AB,CM AND S11(50N)S12(50N)S13
S28         19    S9(50N)S10(50N)S11
S29         18    S10(50N)S11(50N)S12
S30         36    S11(50N)S12(50N)S13
S31         69    S15:S30
S32         27    S31 AND PY=1978:1999
S33         30    S31 AND (AC=US OR AC=US/PR) AND AY=1978:1999
S34         35    S32:S33
S35         35    IDPAT (sorted in duplicate/non-duplicate order)
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35/3,K/1 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01236438
TASK SCHEDULING AND MESSAGE PASSING
TASKREIHENFOLGEPLANUNG UND NACHRICHTENUBERTRAGUNG
ORDONNANCEMENT DE TACHES ET PASSAGE DE MESSAGES
PATENT ASSIGNEE:
Honeywell Inc., (2927097), 101 Columbia Road, P.O. Box 2245, Morristown, New Jersey 07962-2245, (US), (Proprietor designated states: all)
INVENTOR:
BINNS, Pamela, A., 13 Spring Farm Lane, St. Paul, MN 55127, (US)
VESTAL, Stephen, C., 13 Spring Farm Lane, St. Paul, MN 55127, (US)
LEGAL REPRESENTATIVE:
Haley, Stephen (79721), Gill Jennings & Every, Broadgate House, 7 Eldon Street, London EC2M 7LH, (GB)
PATENT (CC, No, Kind, Date): EP 1244963 A2 021002 (Basic)
EP 1244963 B1 031105
WO 2000070455 001123
APPLICATION (CC, No, Date): EP 2000930754 000515; WO 2000US13356 000515
PRIORITY (CC, No, Date): US 312592 990514
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS (V7): G06F-009/46
NOTE:
No A-document published by EPO
LANGUAGE (Publication, Procedural, Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS B (English) 200345 1002
CLAIMS B (German) 200345 889
CLAIMS B (French) 200345 1295
SPEC B (English) 200345 8139
Total word count - document A 0
Total word count - document B 11325
Total word count - documents A + B 11325

...SPECIFICATION internal software-generated events occur. Message values to be received at the dispatch of aperiodic **tasks** are assigned to their input **buffer** variables and the **tasks** are dispatched.
Figure 9 is a **process** flowchart of one embodiment of event handler 620. Figure 9 includes actions boxes 910, 920...

...a software-generated event or external interrupt. Upon receiving the interrupt in action box 910, **event** handler 620 assigns message values to their **task** input buffers in action box 920. The aperiodic task or tasks associated with the interrupt in 910 are dispatched in action box 930. Control is then returned to the highest **priority** ready **task**. As with dispatch **task** 610, dispatching an aperiodic task includes adding the aperiodic task to the ready queue 890...

35/3,K/3 (Item 3 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2008 European Patent Office. All rts. reserv.

00355050
A special purpose processor for off-loading many operating system functions in a large data processing system
Sonderzweckprozessor zur Ubernahme vieler Betriebssystemfunktionen in einem grossen Datenverarbeitungssystem
Processeur a usage special se chargeant de plusieurs fonctions du systeme d'exploitation dans un grand systeme de traitement de donnees.
PATENT ASSIGNEE:
UNISYS CORPORATION, (842793), P.O. Box 500, Blue Bell, PA 19424-0001, (US), (applicant designated states: BE;DE;FR;GB;NL;SE)
INVENTOR:
Jennings, Andrew Thomas, 200 North Deerwood Drive, West Chester Pennsylvania 19382, (US)
Keller, John Allen, 12 Valley Green Drive, Coatesville Pennsylvania 19320

(US)
LEGAL REPRESENTATIVE:
Carmichael, David Andrew Halliday et al (29132), G.F. REDFERN & CO., Redfern House 149/151 Tarring Road, Worthing West Sussex BN11 4HE, (GB)
PATENT (CC, No, Kind, Date): EP 364000 A2 900418 (Basic)
EP 364000 A3 900816
EP 364000 B1 941130
APPLICATION (CC, No, Date): EP 89121835 860926;
PRIORITY (CC, No, Date): US 787781 851015; US 787668 851015; US 787669 851015
DESIGNATED STATES: BE; DE; FR; GB; NL; SE
RELATED PARENT NUMBER(S) - PN (AN):
EP 243402 (EP 869061705)
INTERNATIONAL PATENT CLASS (V7): G06F-009/46;
ABSTRACT WORD COUNT: 169

LANGUAGE (Publication, Procedural, Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS A (English) EPBBF1 954
CLAIMS B (English) EPBBF1 658
CLAIMS B (German) EPBBF1 582
CLAIMS B (French) EPBBF1 763
SPEC A (English) EPBBF1 6122
SPEC B (English) EPBBF1 6069
Total word count - document A 7076
Total word count - document B 8072
Total word count - documents A + B 15148
...SPECIFICATION main memory. More specifically, the basic functions of the special purpose processor are that of **process** or **task** scheduling and the allocation of **events** to such **processes** or **tasks**, which **events** are requested by or affect the execution of the individual **tasks**.
Particularly, such a processor maintains a **queue** of ready or available **processes** linked together according to an assigned priority so that any central processor may be assigned to the highest **priority task** when that processor is not busy executing some higher **priority task**. The special purpose processor also includes a mechanism for computing task priorities as new tasks...

...SPECIFICATION main memory. More specifically, the basic functions of the special purpose processor are that of **process** or **task** scheduling and the allocation of **events** to such **processes** or **tasks**, which **events** are requested by or affect the execution of the individual **tasks**.
Particularly, such a processor maintains a **queue** of ready or available **processes** linked together according to an assigned priority so that any central processor may be assigned to the highest **priority task** when that processor is not busy executing some higher **priority task**. The special purpose processor also includes a mechanism for computing task priorities as new tasks...

35/3,K/5 (Item 5 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01070757
Context controller having event-dependent vector selection and processor employing the same
Kontextsteuerungsvorrichtung mit Auswahl von ereignisabhangigen Vektoren und Prozessor, der diese benutzt
Controleur de contexte avec selection de vecteurs dependants d'evenement et processeur utilisant celui-ci
PATENT ASSIGNEE:
LUCENT TECHNOLOGIES INC., (2143720), 600 Mountain Avenue, Murray Hill, New Jersey 07974-0636, (US), (Applicant designated States: all)
INVENTOR:
Diepstraten, Wilhelmus J.M., Wijnhovenstraat 7, 5089 NX Haghorst, (NL)
Hardell, Wesley D., 7226 Spring Drops Street, San Antonio, Texas 78249-2605, (US)
Fischer, Michael A., 2910 Hunters Horn Street, San Antonio, Texas 78230-5412, (US)
LEGAL REPRESENTATIVE:

Williams, David John et al (86433), Page White & Farrer, 54 Doughty Street, London WC1N 2LS, (GB)
PATENT (CC, No, Kind, Date): EP 942369 A2 990915 (Basic)
APPLICATION (CC, No, Date): EP 99301557 990302;
PRIORITY (CC, No, Date): US 77575 P 980310; US 213618 981217
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS (V7): G06F-009/46
ABSTRACT WORD COUNT: 89
NOTE:
Figure number on first page: 1

LANGUAGE (Publication, Procedural, Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS A (English) 9937 643
SPEC A (English) 9937 13884
Total word count - document A 14527
Total word count - document B 0
Total word count - documents A + B 14527

...SPECIFICATION processor architectures have included a program interruption facility that suspends the execution of a "background" **task**, and initiates the execution of a "foreground" **task**, upon **occurrence** of the exogenous **event** (s). Each program interruption, typically called an "interrupt," causes a reversible change to the execution state of the processor upon assertion (suitably synchronized to the processor's instruction **flow**) of an appropriate **event**.
The **priority** interrupt, developed in the late-1950s, is a common enhancement to a program interruption facility...

35/3,K/8 (Item 8 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01070752
Event-driven and cyclic context controller and processor employing the same
Ereignisgesteuerte und zyklische Kontextsteuerungsvorrichtung und Prozessor, die diese benutzt
Controleur de contexte a commande par evenement et cyclique et processeur utilisant celui-ci
PATENT ASSIGNEE:
LUCENT TECHNOLOGIES INC., (2143720), 600 Mountain Avenue, Murray Hill, New Jersey 07974-0636, (US), (Applicant designated States: all)
INVENTOR:
Diepstraten, Wilhelmus J.M., Wijnhovenstraat 7, 5089 NX Haghorst, (NL)
Fischer, Michael A., 2810 Hunters Horn Street, San Antonio, Texas 78230-5412, (US)
Hardell, Wesley D., 7226 Spring Drops Street, San Antonio, Texas 78249-2605, (US)
LEGAL REPRESENTATIVE:
Williams, David John et al (86433), Page White & Farrer, 54 Doughty Street, London WC1N 2LS, (GB)
PATENT (CC, No, Kind, Date): EP 942366 A2 990915 (Basic)
APPLICATION (CC, No, Date): EP 99301550 990302;
PRIORITY (CC, No, Date): US 77454 P 980310; US 213983 981217
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS (V7): G06F-009/46
ABSTRACT WORD COUNT: 80
NOTE:
Figure number on first page: 1

LANGUAGE (Publication, Procedural, Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS A (English) 9937 563
SPEC A (English) 9937 13767
Total word count - document A 14330

Total word count - document B 0
Total word count - documents A + B 14330

...SPECIFICATION processor architectures have included a program interruption facility that suspends the execution of a "background" **task**, and initiates the execution of a "foreground" **task**, upon **occurrence** of the exogenous **event** (s). Each program interruption, typically called an "interrupt," causes a reversible change to the execution state of the processor upon assertion (suitably synchronized to the processor's instruction **flow**) of an appropriate **event**.
The **priority** interrupt, developed in the late-1950s, is a common enhancement to a program interruption facility...

35/3,K/11 (Item 11 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00964934
Method of dynamic assignment of tasks to events arriving on a set of queues
Verfahren zur dynamischen Zuweisung von Aufgabeprozessen an Ereignisse, die an einer Gruppe von Warteschlangen eintreffen
Procede d'affectation dynamique de taches a des evenements arrivant sur un ensemble de files d'attente
PATENT ASSIGNEE:
ALCATEL, (201874), 54, rue La Boetie, 75008 Paris, (FR), (applicant designated states: AT;BE;CH;DE;ES;GB;IT;LI;NL;SE)
INVENTOR:
Mansion, Jean-Louis, 52 rue de Montval, 78160 Marly le Roi, (FR)
LEGAL REPRESENTATIVE:
Sciaux, Edmond et al (58919), Compagnie Financiere Alcatel, Department D.P.I., 30, avenue Kleber, 75016 Paris, (FR)
PATENT (CC, No, Kind, Date): EP 875829 A1 981104 (Basic)
APPLICATION (CC, No, Date): EP 98401064 980430;
PRIORITY (CC, No, Date): FR 975454 970502
DESIGNATED STATES: AT;BE;CH;DE;ES;GB;IT;LI;NL;SE
INTERNATIONAL PATENT CLASS (V7): G06F-009/46
TRANSLATED ABSTRACT WORD COUNT: 110
ABSTRACT WORD COUNT: 92

LANGUAGE (Publication, Procedural, Application): French; French; French
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS A (French) 9845 149
SPEC A (French) 9845 1378
Total word count - document A 1527
Total word count - document B 0
Total word count - documents A + B 1527

...ABSTRACT Translated)
Dynamic execution of **tasks** and **events** arriving on multiple queues
The dynamic execution system involves using each queue of **tasks** and **events** associated with an indirect identifier. Each time a source wishes to insert a new event...

...identifier for another queue, the source inserts a substitution event containing an identifier of the **queue** in the **queue** indicated by the indirect identifier.
These substitution **events** contain a **priority**, and the **tasks** process the **events** in the **queue** in the order imposed by these priorities. Each **queue** is a FIFO, ie first in, first out, type.

35/3,K/12 (Item 12 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00523935
Preemptive and non pre-emptive scheduling and executing of program threads in a multitasking operating system
Preemptive und nicht-preemptive Ablauffolgeplanung und Ausfuhrung von Programmfaden in einem Multitaskingbetriebssystem

Planification preemptive et non-preemptive, et execution de fils de programme dans un systeme d'exploitation multitache.
PATENT ASSIGNEE:
INTERNATIONAL BUSINESS MACHINES CORPORATION, (200123), , Armonk, NY 10504, (US), (applicant designated states: DE;FR;GB)
INVENTOR:
Farrell, Joel Alan, 395A Cafferty Hill Road, Endwell, NY 13760, (US)
Record, Stephen Elliott, 36 Rolling Ridge Road, Ridgefield, CT 06877, (US)
Wade, Brian Keith, 9 Highland Drive, Apalachin, NY 13732, (US)
LEGAL REPRESENTATIVE:
Jost, Ottokarl, Dipl.-Ing. (6092), IBM Deutschland GmbH Patentwesen und Urheberrecht Schonaicher Strasse 220, W7030 Boblingen, (DE)
PATENT (CC, No, Kind, Date): EP 527392 A2 930217 (Basic)
EP 527392 A3 931013
APPLICATION (CC, No, Date): EP 92112912 920729;
PRIORITY (CC, No, Date): US 743004 910809
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS (V7): G06F-009/46;
ABSTRACT WORD COUNT: 226

LANGUAGE (Publication, Procedural, Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS A (English) EPABF1 2215
SPEC A (English) EPABF1 8905
Total word count - document A 11120
Total word count - document B 0
Total word count - documents A + B 11120

...SPECIFICATION resource, or process is present at one time, they may be queued awaiting the matching **process**, or event or resource, respectively.
US Patent 4,658,351 discloses the use of priority...

...A task control block is generated to represent each task and is stored in the **task queue** corresponding to the **task**'s priority level. Apparently the sequence of the **task** control blocks in each **task queue** is based upon the order in which the corresponding **task** became ready to run. **Tasks** are executed in a sequence depending upon the relative priorities of the **task queues** and upon the locations of the **task** control blocks in each **task queue**. **Event** signalling and message passing are handled by semaphores.
A publication entitled "Scheduling Techniques for Concurrent...

35/3,K/14 (Item 14 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00495522
Process for dispatching tasks among multiple information processors.
Taskzuweisungsverfahren zwischen einer Vielzahl von Informationsprozessoren.
Procede d'attribution des taches entre plusieurs dispositifs de traitement d'information.
PATENT ASSIGNEE:
International Business Machines Corporation, (200120), Old Orchard Road, Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)
INVENTOR:
Bahr, James E., 2103 Folwell Drive S.W., Rochester, Minnesota 55902, (US)
Corrigan, Michael J., 3938 Third Street N.W., Rochester, Minnesota 55901, (US)
Knipfer, Diane L., 3009 15th Avenue N.W., Rochester, Minnesota 55901, (US)
McMahon, Lynn A., 2603 24th Street N.W., Rochester, Minnesota 55901, (US)
Metzger, Charlotte B., P.O. Box 507, Elgin, Minnesota 55932, (US)
LEGAL REPRESENTATIVE:
de Pena, Alain et al (15151), Compagnie IBM France Departement de Proprietelntellectuelle, F-06610 La Gaude, (FR)
PATENT (CC, No, Kind, Date): EP 459931 A2 911204 (Basic)

EP 459931 A3 920610
APPLICATION (CC, No, Date): EP 91480069 910423;
PRIORITY (CC, No, Date): US 531178 900531
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS (V7): G06F-009/46; G06F-015/16;
ABSTRACT WORD COUNT: 258

LANGUAGE (Publication, Procedural, Application): English; English; English

...ABSTRACT 22, 24) have individual cache memories and also share a main storage memory(28), a **process** is disclosed for allocating multiple data operations or **tasks** for subsequent execution by the processing devices. A plurality of **task** dispatching elements (TDE) (96-106) forming a **task** dispatching **queue** (TDQ) 92 are scanned in an order of descending priority, for either a specific affinity...

...has been reached. Following the primary scan a secondary scan, in an order of ascending **priority**, assigns any reserved **tasks** to the processing devices still available, without regard to processor affinity. Previously bypassed **tasks** can be assigned as well, in the **event** that any processor remains available. A further feature of the network is a means to...

35/3,K/15 (Item 15 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00435824
Method of operating a digital computer system
Verfahren zum Betreiben eines Digitalrechnersystems
Methode de fonctionnement d'un systeme d'ordinateur numerique
PATENT ASSIGNEE:
DATA GENERAL CORPORATION, (410941), 4400 Computer Drive, Westboro Massachusetts 01580, (US), (applicant designated states: BE; CH; DE; FR; GB; IT; LI; LU; NL)
INVENTOR:
Farrell, John Michael, Middle Flat, 27 Benson Street, Cambridge, (GB)
Gladstone, Philip John Steuart, 130 Sedgwick Street, Cambridge CB1 3AL, (GB)
LEGAL REPRESENTATIVE:
Jackson, David Spence et al (32231), REDDIE & GROSE 16, Theobalds Road, London, WC1X 8PL, (GB)
PATENT (CC, No, Kind, Date): EP 483421 A1 920506 (Basic)
EP 483421 B1 970402
APPLICATION (CC, No, Date): EP 90311828 901029;
PRIORITY (CC, No, Date): EP 90311828 901029
DESIGNATED STATES: BE; CH; DE; FR; GB; IT; LI; LU; NL
INTERNATIONAL PATENT CLASS (V7): G06F-009/46; H04L-012/58;
ABSTRACT WORD COUNT: 151

LANGUAGE (Publication, Procedural, Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS A (English) EPABF1 2992
CLAIMS B (English) EPAB97 2015
CLAIMS B (German) EPAB97 1907
CLAIMS B (French) EPAB97 2444
SPEC A (English) EPABF1 17262
SPEC B (English) EPAB97 17751
Total word count - document A 20255
Total word count - document B 24117
Total word count - documents A + B 44372

...SPECIFICATION GUI, there is no polling at all, because GUI automatically waits for all possible external **events** whenever there are no **jobs** to run. This results in applications which use virtually no CPU time when they are...

...that an external event has occurred
Notification that an interval of time has elapsed
Inter-**process** communication (IPC), that is to say a message passed

between two computer **processes**.
In principle there need only be one **job** queue but an application can set up multiple **queues** with different priorities and select the **queues** on to which it or service providers place **jobs**, as a means of controlling **priority** of execution of **jobs**. The application (user) calls a run routine gui...

...SPECIFICATION GUI, there is no polling at all, because GUI automatically waits for all possible external **events** whenever there are no **jobs** to run. This results in applications which use virtually no CPU time when they are...

...that an external event has occurred
Notification that an interval of time has elapsed
Inter-**process** communication (IPC), that is to say a message passed between two computer **processes**. In principle there need only be one **job** queue but an application can set up multiple **queues** with different priorities and select the **queues** on to which it or service providers place **jobs**, as a means of controlling **priority** of execution of **jobs**. The application (user) calls a run routine gui(underscore)run() when it is free for...

...call gui(underscore)run(), with a pend option, whereby gui(underscore)run() pends until a **job** is scheduled by an inter-process communication (IPC), an **event**-handler service, denoted GUES below, or a timer service, denoted GUTS below. This assumes that...

...CLAIMS in sets allocated to a plurality of different owners and wherein each owner can only **call** said run **routine** in respect of a queue which that owner owns.
24. A digital computer system according...

...in sets allocated to a plurality of different owners and wherein each owner can only **call** said run **routine** in respect of a queue which that owner owns, and wherein said data structure further...

...CLAIMS in sets allocated to a plurality of different owners and wherein each owner can only **call** said run **routine** in respect of a queue which that owner owns.
24. A digital computer system according...

...in sets allocated to a plurality of different owners and wherein each owner can only **call** said run **routine** in respect of a queue which that owner owns, and wherein said data structure further...

35/3,K/18 (Item 18 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

00299401
Method for treating work calls to one of the processes from the single processes of a data processing system
Verfahren zur Behandlung der von den einzelnen Prozessen einer Datenverarbeitungsanlage verursachten Arbeitsaufrufe an einen der Prozesse.
Methode pour le traitement des appels de travail a un processus par les processus singuliers d'un systeme de traitement de donnees.
PATENT ASSIGNEE:
Siemens Nixdorf Informationssysteme AG, (220702), Furstenallee 7, D-33102 Paderborn, (DE), (applicant designated states: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE)
INVENTOR:
Hulters, Hubert, Von-Eichendorff-Strasse 14, W-8152 Feldkirchen/Westerham (DE)
LEGAL REPRESENTATIVE:
Fuchs, Franz-Josef, Dr.-Ing. et al (3891), Postfach 22 13 17, W-8000 Munchen 22, (DE)
PATENT (CC, No, Kind, Date): EP 360900 A1 900404 (Basic)
EP 360900 B1 930602
APPLICATION (CC, No, Date): EP 88116011 880928;
PRIORITY (CC, No, Date): EP 88116011 880928

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE
INTERNATIONAL PATENT CLASS (V7): G06F-009/46;
TRANSLATED ABSTRACT WORD COUNT: 194
ABSTRACT WORD COUNT: 132

LANGUAGE (Publication, Procedural, Application): German; German; German
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS B (English) EPBBF1 1913
CLAIMS B (German) EPBBF1 2533
CLAIMS B (French) EPBBF1 2033
SPEC B (German) EPBBF1 8379
Total word count - document A 0
Total word count - document B 14858
Total word count - documents A + B 14858

...CLAIMS B1
1. Method for **handling** job **calls**, caused by the individual **processes** of a data processing system, to one of the processes, in which the job calls...
...forwarding the job call having the highest priority level in each case to the associated **process**, if necessary with simultaneous interruption of a current **process** initiated earlier by a **job** call having a lower priority level,
characterised
- in that a system of a plurality of **queues** (WS...) operating according to the FIFO principle is used at least in one of the **process** levels (for example PZE4) for the temporary storage of **job** calls (WR) in the individual **process** levels (for example PZE0 to PZE7), of which each **queue** only accepts **job** calls having the same priority level and can be identified directly on the basis of the call instruction (XGOLxx) invoking the **job** call,
- in that the entry of the individual **job** calls (WR) into the **queues** of said **queue** system is effected by the respective **process** (PZi) invoking the **job** call,
- in that the memory elements (EL...) forming the respective **queue** have in each case a control entry (AD-UP1/UP(sub(ELNK))) which can be...

35/3,K/19 (Item 19 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00299398
Method and apparatus for treating interrupt requests and process calls in a combined interrupt and sequence controlled system in data processing systems working
Verfahren und Anordnung zur Behandlung von Unterbrechungsanforderungen und Prozessaufrufen in einem kombinierten Unterbrechungs- und Ablaufsteuersystem fur weni
Methode et dispositif pour le traitement des demandes d'interruption et des appels de processus dans un systeme combine d'interruptions et de commande sequentie
PATENT ASSIGNEE:
Siemens Nixdorf Informationssysteme AG, (220702), Furstenallee 7, D-33102 Paderborn, (DE), (applicant designated states: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE)
INVENTOR:
Hullers, Hubert, Von-Eichendorff-Strasse 14, W-8152 Feldkirchen/Westerham, (DE)
LEGAL REPRESENTATIVE:
Fuchs, Franz-Josef, Dr.-Ing. et al (3891), Postfach 22 13 17, W-8000 Munchen 22, (DE)
PATENT (CC, No, Kind, Date): EP 360897 A1 900404 (Basic)
EP 360897 B1 930630
APPLICATION (CC, No, Date): EP 88116008 880928;
PRIORITY (CC, No, Date): EP 88116008 880928
DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE
INTERNATIONAL PATENT CLASS (V7): G06F-009/46;
TRANSLATED ABSTRACT WORD COUNT: 192
ABSTRACT WORD COUNT: 151

LANGUAGE (Publication, Procedural, Application): German; German; German
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1970
CLAIMS B	(German)	EPBBF1	1588
CLAIMS B	(French)	EPBBF1	2172
SPEC B	(German)	EPBBF1	4361
Total word count - document A			0
Total word count - document B			10091
Total word count - documents A + B			10091

...CLAIMS program-related, that signify interrupt levels, divided according to priorities in the same way, for **job** calls to the processes program-related job calls being entered in **queues** (WS-ME, WS-SVC, ...),
- having means for monitoring and evaluating the priority level of the events signifying a job call for one of the **processes**, and
- having means (IDEC) for forwarding the job call having the highest priority level in each case to the associated **process**, if necessary with simultaneous interruption of a current **process** initiated earlier by a **job** call having a lower priority level, characterised
- in that a plurality of **queues** operating according to the FIFO principle are available at least for one of the **process** levels (SYST1), so that program-related **job** calls (WR) initiated by the respective current **process** for one of the processes of said process level (SYST1) are entered in one of...

...queues by said current process, said one queue being obtained by fixed assignment to the **process** level (ZBH) of the calling **process**,
- in that, after the current **process** has entered the respective **job** call (WR) in the appropriate **queue** (WS-ZBH), in addition an event (i(sub(px))) signifying the presence of the **job** call (WR) is indicated before the current **process** continues its program
- in that, independently of the current process, the last event (i(sub...
...example PZE0 to PZE7), characterised in that the means (IDEC) for monitoring and evaluating the **priority** level of the one **job** call (WR) for **events** (i(sub(xx))) signifying one of the processes in each case monitor and evaluate all...6. Method according to Claim 5, characterised
- in that upon the release of an indicated **event** (for example i(sub(p4))) for program-related job calls (WR) for handling (message INT...

...level than the current process, temporarily stored job calls (WR) are only removed from the **queues** and the associated process called if the previously called **process** has terminated its running status and has released the processing unit again (message RLC),
- in...

...necessary acknowledgement in the course of the execution of a job call by a called **process** is made by the called **process** to the **process** initiating the **job** call likewise in the form of a **job** call,
- in that each **job** call (WR) removed from the **queues** is transferred without conditions to the associated **process** as if the latter were executable,
- in that such a transferred **job** call is entered in a **process**-internal **queue** if it was not hitherto possible to execute the **task** associated with a preceding **job** call for the same **process** and as a result the processing unit (CPU) had been released for other processes in...

...7, characterised in that the release of events (for example i(sub(px))) signifying work **calls** for **handling** is controlled by an adjustable mask which is set to the priority of a process...

...program-related, that signify interrupt levels, divided according to priorities in the same way, for **job** calls to the processes program-related **job** calls being entered in **queues** (WS-ME, WS-SVC, ...),

- having means for monitoring and evaluating the priority level of the events signifying a **job** call for one of the **processes**, and
- having means (IDEC) for forwarding the job call having the highest priority level in each case to the associated **process**, if necessary with simultaneous interruption of a current **process** initiated earlier by a **job** call having a lower priority level, characterised by a **queue** arrangement (WS-SYST0 to WS-SYST7) structured in accordance with the **process** levels (PZE0 to PZE7) which has at least in one of the process levels a number of queues operating according to the FIFO principle corresponding to the number of **process** levels, in which in each case one of said **queues** is individually assigned to one of the **process** levels and is provided only for the entry of **job** calls (WR) of **processes** from the associated **process** level, and in which each of said **queues** can be addressed directly by the combination of the priority (PRIO(sub(PRIOZ))) of the...

35/3,K/23 (Item 23 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00755411 **Image available**
APPARATUS AND PROCESS FOR ELECTRONIC FILING OF FORMS
DISPOSITIF ET PROCEDE ELECTRONIQUES DE CLASSEMENT DE FORMULAIRES
Patent Applicant/Assignee:
TRANSENDA INTERNATIONAL LLC, 8730 148th Avenue NE, Redmond, WA 98052, US, US (Residence), US (Nationality)
Inventor(s):
HOME Teri Ann, 2108 186th Place SE, Bothell, WA 98012, US
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SCHEFFER Brian, 14319 275th Avenue NE, Duvall, WA 98017, US
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CROVITZ Michael, 12811 NE Marine View Drive, Kingston, WA 98346, US
HAWES Lloyd, 208 Garfield Street, Seattle, WA 98109, US
Legal Representative:
BECKER Todd M, Davis Wright Tremaine LLP, 2600 Century Square, 1501 Fourth Avenue, Seattle, WA 98101-1688, US
Patent and Priority Information (Country, Number, Date):
Patent: WO 200068819 A1 20001116 (WO 0068819)
Application: WO 2000US12841 20000510 (PCT/WO US0012841)
Priority Application: US 99309020 19990510
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 14187

Fulltext Availability:
Detailed Description
Claims

Detailed Description
... improve the reliability of a very critical area of the filmor center. In operation, the **process** scheduler receives notification from the event system (described below) that a new **job** has been added to the **queue** or is alerted that a command **process** **thread** has finished. The **process** scheduler polls command **processes** for availability.

The process scheduler assigns the highest **priority**, older **jobs** needing the available command processes by changing their status to Processing and passing the Job identification numbers (JobID) to the

command **processes** . If a **thread** has finished, the **process** scheduler changes the status of the finished 'ob in the **Job queue** to Completed.

J J
In addition, the **process** scheduler checks to see that all active **jobs** are still within acceptable time allowances set for each command **process** . If any active 'ob goes outside the set time allowance, the process scheduler sends the...

Claim
... 2 wherein the device pool comprises modems, an ISDN line, an internet connection, or a **voice response** system

4 The forril fillnLy center of claim I wherein the command neLyotiator throttles or...19 wherein the device pool comprises modems, an ISDN line, an internet connection. or a **voice response** svstern.

21 The formfilinu center of claim 19 wherein the command neLiotiator throttles or...

35/3,K/24 (Item 24 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00561878 **Image available**
SYSTEM AND METHOD FOR DETECTING PURCHASING CARD FRAUD
SYSTEME ET PROCEDE DE DETECTION DE FRAUDES LIEES AUX CARTES D'ACHAT
Patent Applicant/Assignee:
FIRST DATA CORPORATION,
Inventor(s):
GESCHWENDER Julie A,
MURPHY-HOUSER Michele,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200025251 A1 20000504 (WO 0025251)
Application: WO 99US24836 19991025 (PCT/WO US9924836)
Priority Application: US 98105611 19981026
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
CA CN JP MX AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
Publication Language: English
Fulltext Word Count: 3334

Fulltext Availability:
Detailed Description

Detailed Description
... stages of a purchasing card life cycle.

Application Processing
Card Activation
Cardholder Usage/Maintenance
Other **Transaction** or Contact **Events** : **Priority** Non-Mons.

PIN changes, plastic requests, credit line increases and changes to the account record...

...of the invention are.

Consortium Data Warehouse
Fraud Scoring
Actioning (Alerts to On-Line Screens)
Queuing for Manual Review
The Matchini4 **Process**
As shown in Figure 3, selected non-monetary **transactions** may be structured to create queries which compare account record data elements against the Consortium..

35/3,K/25 (Item 25 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT

00543762 **Image available**

METHOD FOR PREDICTIVE ROUTING OF INCOMING CALLS WITHIN A COMMUNICATION CENTER ACCORDING TO HISTORY AND MAXIMUM PROFIT/CONTRIBUTION ANALYSIS

PROCEDE D'ACHEMINEMENT PREDICTIF D'APPELS ENTRANTS DANS UN CENTRE DE COMMUNICATION EN FONCTION DE L'HISTORIQUE ET D'UNE ANALYSE DE PROFIT/CONTRIBUTION MAXIMAUX

Patent Applicant/Assignee:
GENESYS TELECOMMUNICATIONS LABORATORIES INC,
Inventor(s):
SHENKMAN Grigory,

Patent and Priority Information (Country, Number, Date):
Patent: WO 200007135 A1 20000210 (WO 0007135)
Application: WO 99US16288 19990726 (PCT/WO US9916288)
Priority Application: US 98127284 19980731
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZW GH GM KE LS MW SD SL SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
Publication Language: English
Fulltext Word Count: 7968

METHOD FOR PREDICTIVE ROUTING OF INCOMING CALLS WITHIN A COMMUNICATION CENTER ACCORDING TO HISTORY AND MAXIMUM PROFIT/CONTRIBUTION ANALYSIS

Detailed Description
... an automated attendant such as an automated fax or alternative IVR attendant.

Perhaps a lower **priority routing** to an information agent may be the determination.

Any interaction results are subsequently added to HDB 61 as part of the contact **history** of that client. In any **event**, a complete **transaction history** including any agent/client interaction result is developed, stored and maintained in HDB 61 as...

Claim
... 52. The method of claim 1 wherein the hosted communication network is a telecommunication **call center**.

3 The method of claim 1 wherein, in step (a) past customers are assigned a...

...to the data repository. ....
13 15
PSTN
SW 2 1
Internet
23 4 25

26
IVR
27 SW 30
T
rin 01
56
MIS 55
---
7 --- 5 1
57 - 49

Lo...

...Figo I
15
PSTN
SW21
[lin Internet
23 4 25

---
1 26
27
60 **IVR** 65
61 @@SWk@@28
I
'o
HDB
63 S/STA
[IF]
PDB
56
55...

35/3,K/26 **(Item 26 from file: 349)**
DIALOG(R)File 349:PCT FULLTEXT
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00526312 **Image available**
SYSTEMS AND METHODS FOR AUTOMATED ORDER PROCESSING
SYSTEMES ET PROCEDES POUR LE TRAITEMENT AUTOMATIQUE DES COMMANDES
Patent Applicant/Assignee:
MARCAM SOLUTIONS INC,
Inventor(s):
DALTON John T,
RYAN William,
TRIGG John,
HOWELLS Richard,
DRUMMOND Laurel,
O'BRIEN Matthew,
Patent and Priority Information (Country, Number, Date):
Patent: WO 9957664 A1 **19991111**
Application: WO 99US9017 19990426 (PCT/WO US9909017)
Priority Application: US 9884201 19980504; US 98108115 19980630; US 99248794 19990212
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
CA JP AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
Publication Language: English
Fulltext Word Count: 55661

Patent and Priority Information (Country, Number, Date):
Patent: ... **19991111**
Fulltext Availability:
Detailed Description

English Abstract
Systems and methods for automated **transaction** processing utilize modifiable tables that define **significant events** in **transaction flow** and that define actions to be taken in response to those events. In addition to...
Publication Year: **1999**

Detailed Description
... The foregoing objects are met by the invention, which provides systems and methods for automated **transaction** processing utilizing modifiable tables that define **significant events** in **transaction flow** and that define actions to be taken in response to those events. In addition to...

...transaction processing that comprises objects, or other constructs,
storing status and other information about respective **transactions**.
e.g., customer orders. An **event** generator crenerates **event**
notifications in response to selected changes made in those objects.
e.g., by a user...

35/3,K/29 (Item 29 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00438698 **Image available**
METHOD AND APPARATUS FOR MANAGING FAULTS AND EXCEPTIONS
PROCEDE ET APPAREIL PERMETTANT DE GERER DES DEFAILLANCES ET DES ANOMALIES
Patent Applicant/Assignee:
SILICON GAMING INC,
PASCAL Andrew,
BARNETT Michael,
WISHOFF Clayton,
Inventor(s):
PASCAL Andrew,
BARNETT Michael,
WISHOFF Clayton,
Patent and Priority Information (Country, Number, Date):
Patent: WO 9829162 A1 **19980709**
Application: WO 97US23940 19971224 (PCT/WO US9723940)
Priority Application: US 96774826 19961227
Designated States:
(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)
AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH GM
GW HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX
NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU ZW GH
GM KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH DE DK ES FI
FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG
Publication Language: English
Fulltext Word Count: 5275

Patent and Priority Information (Country, Number, Date):
Patent: ... **19980709**
Fulltext Availability:
Detailed Description

English Abstract
...system initiates a service call for an attendant through one of the
provided mechanisms or **routine calls** (fig. 4). The system initiates
an intermission or play stoppage routine with full audio and...
Publication Year: **1998**

Detailed Description
... by various driver routines when they detect an event that the event
manager needs to **process**. The **event** manager is implemented as a very
high **priority task** that polls the **queue** for **events**. When an
event is accepted by the event manager 89, it is evaluated along with
all other outstanding...

35/3,K/30 (Item 30 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00382184 **Image available**
JOB SCHEDULING FOR INSTRUCTION PROCESSOR
PROGRAMMATION DE TRAVAUX DESTINEE A UNE UNITE DE TRAITEMENT D'INSTRUCTIONS
Patent Applicant/Assignee:
TELEFONAKTIEBOLAGET LM ERICSSON (publ),
Inventor(s):
RONSTROM Mikael,
Patent and Priority Information (Country, Number, Date):
Patent: WO 9722927 A1 **19970626**
Application: WO 96SE1706 19961219 (PCT/WO SE9601706)
Priority Application: US 95574977 19951219

Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE HU IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TR TT UA UG UZ VN KE LS MW SD SZ UG AM AZ BY KG KZ MD RU TJ TM AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG
Publication Language: English
Fulltext Word Count: 13320
Patent and Priority Information (Country, Number, Date):
Patent: ... **19970626**
Fulltext Availability:
Detailed Description
Publication Year: **1997**

Detailed Description
... level of the previously interrupted job); and
the interrupted job is resumed. Resumption of
interrupted **job** execution by signal processor 124 creates
IP event E(IP)2, understood with reference to ...ID of the fetched job.
At step 8(I)-8,
the thread IDs of all **jobs** in the **job buffers** of **priority**
level "C" or higher are checked and, if a **job**'s thread ID
is the same as the CurrentThreadID (i.e, the **thread** ID of
the fetched job), then a signal associated with each such
job is sent...

...122. In other words, an IP event E(IP)11 is generated
for each such **job** in the **job buffer** (s) (of level " C" or
higher) which has a **thread** ID which is the same as
CurrentThreadID. Action taken by instruction processor
in response to...

...A(SP)I then terminates
(as indicated by symbol 8(1)-9)

If the fetched **job** has **priority** level "D" (as
determined at step 8(1)-4), then at step 8(1)-10...

...Job
interrupted
level[C] are
consulted to determine if execution by instruction
processor 122 of **jobs** of either **priority** level "C" or "D"
are currently ...SP)1. At step 8(1)-18, flag Active
priority is set to
be the **priority** of the job fetched at step 8(i)-I;
CurrentThreadID is set to the thread...to step 8(1)-7. At step
8(1)-18, the thread IDs of all **jobs** in the **job buffers**
(having **priority** level of "C" or higher) are checked and,
if a **job**'s **thread** ID is the same as the CurrentThreadID
(i.e, the **thread** ID of the fetched job), then a signal
associated with each such job is sent...
...122. In other words, a IP event
E(IP)11 is generated for each such **job** in the **job buffer**
which has a **thread** ID which is the same as
CurrentThreadID. As ...event
E(IP)11 is described with reference to Fig. 9(11)

If the fetched **job** has a **priority** level other
than "C" or "D", then steps 8(1)-19 and 8(1)-20...

...similar to steps 8(1)-17 and
8(1)-18, and essentially result in all **jobs** in the **buffer**
which have a **thread** ID which is the same as the
CurrentThreadID being sent to the

35/3,K/35 (Item 35 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT

00137626 **Image available**
A SPECIAL PURPOSE PROCESSOR FOR OFF-LOADING MANY OPERATING SYSTEM FUNCTIONS IN A LARGE DATA PROCESSING SYSTEM
PROCESSEUR SPECIALISE POUR LE DECHARGEMENT DE NOMBREUSES FONCTIONS D'UN SYSTEME DE FONCTIONNEMENT DANS UN GRAND SYSTEME DE TRAITEMENT DE DONNEES
Patent Applicant/Assignee:
BURROUGHS CORPORATION,
Inventor(s):
JENNINGS Andrew Thomas,
KELLER John Allen,
Patent and Priority Information (Country, Number, Date):
Patent: WO 8702486 A1 **19870423**
Application: WO 86US2018 19860926 (PCT/WO US8602018)
Priority Application: US 85668 19851015; US 85669 19851015; US 85781 19851015
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AT BE CH DE FR GB IT JP LU NL SE
Publication Language: English
Fulltext Word Count: 8347

Patent and Priority Information (Country, Number, Date):
Patent: ... **19870423**
Fulltext Availability:
Detailed Description
Publication Year: **1987**

Detailed Description
... main memory.

More specifically, the basic functions of the special purpose processor are that of **process** or **task** scheduling and the allocation of **events** to such **processes** or **tasks**, which **events** are requested by or affect the execution of the individual **tasks**.

Particularly, such a processor maintains a **queue** of ready or available **processes** linked together according to an assigned priority so that any central processor may be assigned to the highest **priority** **task** when that processor is not busy executing some higher **priority** **task**. The special purpose processor also includes a mechanism for computing task priorities as new tasks...